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Patent Number	7,071,014				
Issue Date	7/4/2006				
First Named Inventor	Currie et al.				
Title	Methods for Preserving Strained Semiconductor Substrate Layers during CMOS Processing				
Attorney Docket Number	TSM09-043US-AW				

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1 17/1 1	Firm or Individual Name	Taiwan Semiconductor Manufacturing Com	pany, Ltd.								
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	Applicant/Invento	r									
X		d of the entire interest. See 37 CFR 3.71. 37 CFR 3.73(b) (Form PTO/SB/96) submitted	d herewith o	r filed on			.				
		SAGNATURE of Applican	t or Assign	ee of Record							
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Name	Keith	Chango		Telephone			, , , , , , , , , , , , , , , , , , , 				
Title and	1.0	etor of Intellectual Property, Taiwan Semicono	ductor Manu	facturing Company. Life	d.						
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.											
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